

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Attorney Docket No: LAM2P246

Gotkis et al.

Examiner: H.K. Vu

Application No: 09/821,415

Group Art Unit: 2811

Filed: March 28, 2001

Date: November 22, 2002

For: SEMICONDUCTOR STRUCTURE IMPLEMENTING)  
 LOW-K DIELECTRIC MATERIALS AND SUPPORTING )  
 STUBS (As Amended) )



## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail to: Commissioner for Patents, Washington, DC 20231 on November 22, 2002.

Signed: \_\_\_\_\_

Kay Harlow

Commissioner for Patents  
 Washington, DC 20231

Sir:

Transmitted herewith are the Amendment in Response to Restriction Requirement, and Marked-Up Claims document in the above-identified application.

The fee has been calculated as shown below.

	Claims Remaining After Amendment	Highest Previously Paid For	Present Extra	SMALL ENTITY RATE FEE	OR	LARGE ENTITY RATE FEE
TOTAL CLAIMS	12 -	20	00	X09 = \$	OR	X18 = \$
INDEP CLAIMS	03 -	04	00	X42 = \$	OR	X84 = \$

[ ] Multiple Dependent Claim Present  
 and Fee Not Previously Paid

\$140

\$280

TOTAL

\$ \_\_\_\_\_

\$ \_\_\_\_\_

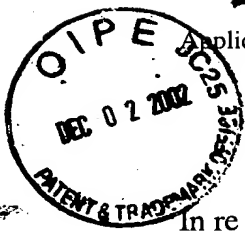
- ☐ Applicant(s) hereby petition for a \_\_\_\_\_ month(s) extension of time to respond to the outstanding Office Action.
- ☒ Applicant(s) believe that no (additional) Extension of Time is required; however, if it is determined that such an extension is required, Applicant(s) hereby petition that such an extension be granted and authorize the Commissioner to charge the required fees for an Extension of Time under 37 CFR 1.136 to Deposit Account No. 50-0805.
- ☐ Enclosed is our Check No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_ to cover the additional claim fee and/or extension of time fees.
- ☒ If the required fees are missing or any additional fees are required to facilitate filing the enclosed response, please charge such fees or credit any overpayment to Deposit Account No. 50-0805 (Order No. LAM2P246). A copy of this sheet is enclosed.

Respectfully submitted,  
 MARTINE & PENILLA, LLP

Albert S. Penilla, Esq.  
 Registration No. 39,487

710 Lakeway Drive, Suite 170  
 Sunnyvale, CA 94085  
 Telephone: (408) 749-6900  
 Customer Number 25920

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Application No. 09/821,415

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

12/5/02  
John H

In re the application of: )  
 )  
 Gotkis et al. )  
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 Application No: 09/821,415 )  
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 Filed: March 28, 2001 )  
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 For: SEMICONDUCTOR STRUCTURE )  
 IMPLEMENTING LOW-K DIELECTRIC MATERIALS )  
 AND SUPPORTING STUBS (As Amended) )  
 )  
 \_\_\_\_\_ )

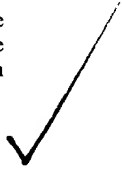
Atty. Docket: LAM2P246  
 Examiner: H.K. Vu  
 Group Art Unit: 2811  
 Date: November 22, 2002

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I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on November 22, 2002.

Signed: \_\_\_\_\_

Kay Harlow



AMENDMENT IN RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

This is in response to the Office Action, dated October 23, 2002. Please enter the following amendments and remarks:

IN THE CLAIMS

Please amend the claims as follows. All pending claims after this Amendment are in clean form and are listed below for the convenience of the Examiner. Attached hereto as a separate document is the "Marked-Up Claims," showing amendments using bracketing and underlining.

**Please delete independent claim 21 and new claims 22-25.**

1. A semiconductor device, comprising:  
a substrate having transistor devices;

